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PATENT
Attorney Docket No. 3180.0265

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
TAKU UCHINO)
Application No.: 09/661,919) Group Art Unit: 2128
Filed: September 14, 2000) Examiner: F. Ferris III
For: METHOD FOR ESTIMATING)
POWER CONSUMPTION AND)
NOISE LEVELS OF AN)
INTEGRATED CIRCUIT, AND)
COMPUTER-READABLE)
RECORDING MEDIUM STORING)
A PROGRAM FOR ESTIMATING)
POWER CONSUMPTION AND)
NOISE LEVELS OF AN)
INTEGRATED CIRCUIT)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(i)

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(d), applicant brings to the attention of the Examiner the documents listed on the attached PTO 1449. This Information Disclosure Statement is being filed after payment of the Issue Fee . Applicant respectfully requests that the Information Disclosure Statement be placed in the file and understand that it will not be considered by the Patent Office.

The article by Uchino and three (3) Japanese patent publications, listed on the attached Form PTO/SB/08, were first cited in a Notice of Grounds for Rejection issued by the Japanese Patent Office in a counterpart Japanese application, mailed April 11, 2006. A copy of the Notice and an English-language version thereof are attached.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the documents as prior art against any claims in the application and applicants determine that the cited documents do not constitute "prior art" under United States law, applicants reserve the right to present to the office the relevant facts and law regarding the appropriate status of such documents.

Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

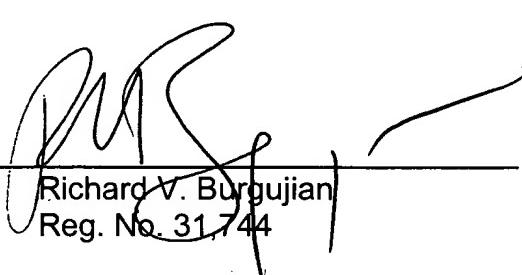
If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

Dated: 5/18/06

By:


Richard V. Burgujian
Reg. No. 31,744

MAY 18 2006

Complete if Known**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet

1

of

1

Application Number

09/661,919

Filing Date

September 14, 2000

First Named Inventor

Taku Uchino

Art Unit

2128

Examiner Name

F. Ferris III

Attorney Docket Number

3180.0265

Sheet	1	of	1	Attorney Docket Number	3180.0265
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U.S. PATENTS AND PUBLISHED U.S. PATENT APPLICATIONS

Examiner Initials'	Cite No. ¹	Document Number	Issue or Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US-			

Note: Submission of copies of U.S. Patents and published U.S. Patent Applications is not required.**FOREIGN PATENT DOCUMENTS**

Examiner Initials'	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Translation ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
		JP 9-245081	9/19/97	Nippon Telegraph & Telephone Corp.		Abstract
		JP 8-249372	9/27/96	Toshiba Corp.		Abstract
		JP 8-153127	6/11/96	Hitachi Ltd.		Abstract

NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Translation ⁶
		Notice of Grounds for Rejection issued by the Japanese Patent Office mailed April 11, 2006, for Japanese Patent Application No. 11-264538, and English-language translation thereof	
		UCHINO, "METHOD FOR ESTIMATING POWER CONSUMPTION OF AN INTEGRATED CIRCUIT," Toshiba Technology Publications (August 29, 1996), 14-46:1-2	

Examiner Signature	Date Considered
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.